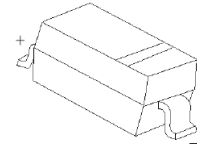




## SOD-323 Plastic-Encapsulate Diodes

### **CESD5V0D3** ESD Protection Diode

SOD-323



#### DESCRIPTION

The CESD5V0D3 is designed to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space is at a premium.

#### FEATURES

- Stand-off Voltage:5.0 V
- Low Leakage
- Response Time is Typically < 1 ns
- ESD Rating of Class 3 (> 16 kV) Per Human Body Model
- IEC61000-4-2 Level 4 ESD Protection
- These are Pb-Free Devices

#### Maximum Ratings @Ta=25°C

Parameter	Symbol	Limit	Unit
IEC61000-4-2(ESD) Air Contact		±15 ±8.0	KV
ESD Voltage per human body model		30	KV
Total Power Dissipation on FR-5 Board (Note 1)	<b>P<sub>D</sub></b>	200	mW
Thermal Resistance Junction-to-Ambient	R <sub>ΘJA</sub>	625	°C/W
Lead Solder Temperature – Maximum (10 Second Duration)	<b>T<sub>L</sub></b>	260	°C
Junction and Storage Temperature Range	<b>T<sub>j</sub>, T<sub>stg</sub></b>	-55 ~ +150	°C

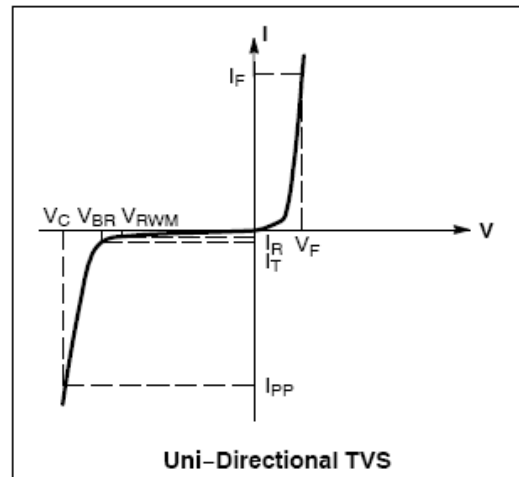
Stresses exceeding maximum ratings may damage the device. Maximum ratings are stress ratings only.

Functional operation above the recommended. Operating conditions is not implied. Extended exposure to stresses above the recommended operating conditions may affect device reliability.

1. FR-5 = 1.0 x 0.75 x 0.62 in.

**ELECTRICAL CHARACTERISTICS** (Ta= 25°C unless otherwise noted)

Symbol	Parameter
$I_{PP}$	Maximum Reverse Peak Pulse Current
$V_C$	Clamping Voltage @ $I_{PP}$
$V_{RWM}$	Working Peak Reverse Voltage
$I_R$	Maximum Reverse Leakage Current @ $V_{RWM}$
$V_{BR}$	Breakdown Voltage @ $I_T$
$I_T$	Test Current
$I_F$	Forward Current
$V_F$	Forward Voltage @ $I_F$
$P_{pk}$	Peak Power Dissipation
C	Max. Capacitance @ $V_R=0$ and $f=1\text{MHz}$



**ELECTRICAL CHARACTERISTICS** (Ta = 25°C unless otherwise noted,  $V_F = 0.9\text{ V Max. @ } I_F = 10\text{mA}$  for all types)

Device*	Device Marking	$V_{RWM}$	$I_R$ ( $\mu\text{A}$ )	$V_{BR}$ (V)		$I_T$	$V_C$	$I_{PP}$ (A) +	$V_C$ (V)	$P_{pk}$ +	C
		(V)	@ $V_{RWM}$	@ $I_T$ (Note 2)			@ $I_{PP} = 5\text{ A}$		@ $\text{Max } I_{PP}$ +		
		Max	Max	Min	Max	mA	V	Max	Max	Max	Typ
<b>CESD5V0D3</b>	ZA	5.0	10	6.2	7.3	1.0	9.8	15	15.5	350	350

\*Other voltages available upon request.

+Surge current waveform per Figure 6.

2.  $V_{BR}$  is measured with a pulse test current  $I_T$  at an ambient temperature of 25°C.